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10/791,924

PATENT  
Conf. No.: 3818

IN THE UNITED STATES PATENT OFFICE

Applicants: Brian T. Brunn et al.  
Assignee: Xilinx, Inc.  
Title: BIT-EDGE ZERO FORCING EQUALIZER  
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AMENDMENT IN RESPONSE TO FINAL OFFICE ACTION

In response to the final Office Action mailed from the Patent Office on April 28, 2009, (the “Office Action”) reconsideration and allowance of all claims is respectfully requested in view of the amendments and remarks that follow.

**Amendments to the Claims** begin on page 2 of this paper.

**Remarks** begin on page 7 of this paper.